SLR Systems

SLRMAC

INTEL MNEMONIC RELOCATING MACRO ASSEMBLER

HIGH PERFORMANCE SOFTWARE

If you would like to assemble your existing M80, MAC, or RMAC files six times faster than your current assembler can do the same job, and to do it with little or no changes in your existing source files, SLRMAC from SLR Systems is the answer.

If you find the prospect of getting as many assembly turnarounds accomplished by Monday afternoon that currently takes M80 until Friday afternoon, then you will like using SLRMAC from SLR.

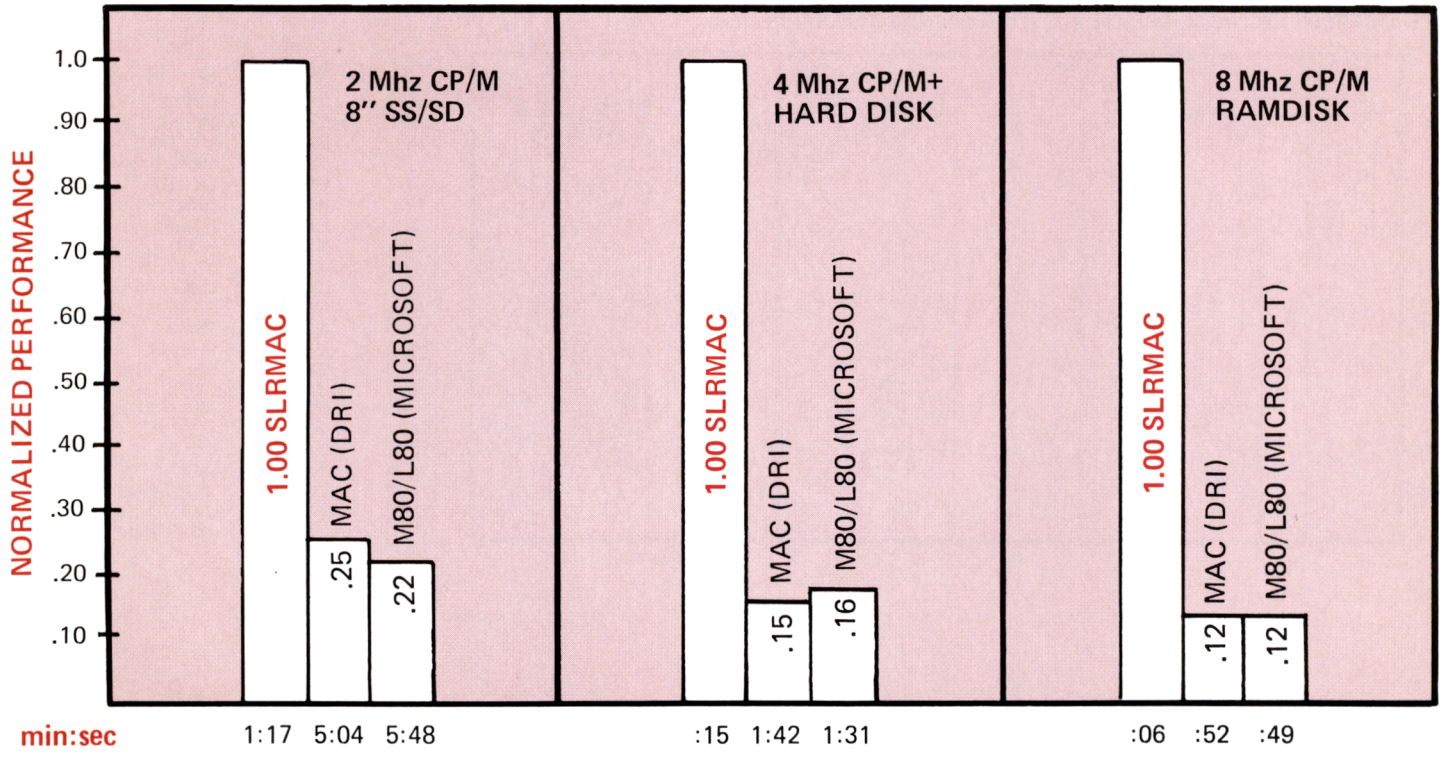
If you are tired of waiting for your assembler to assemble the output from your C compiler, you will be pleasantly surprised with SLRMAC.

SLRMAC and SLRMAC+ (the disk-based version) represent the two fastest and most powerful Intel mnemonic assemblers on the market. Beyond their incredible speed, these mainframe caliber products tout an unparalleled collection of features such as the ability to produce COM, REL, or HEX file output directly, nested conditionals, nested include files, user selectable upper/lower case significance, labels significant to 16 characters, full Intel mnemonic support with the Z80.LIB extensions built in, time and date in listings and much more.

To provide all of the power available in these products in an easy to use form, SLR provides a configuration utility with each assembler that lets you custom tailor the defaults to best suit your needs. These configuration options include the ability to specify printer page length and width, printer initialization and termination control strings, error abort and error list limits, and default file extensions.

Professional assembly language programmers will appreciate finding all of the following features in one product: RMAC/M80 macro support, up to 15 separate program, data, and common areas, sorted symbol table, sorted cross-reference, operator overview of the assembly process, phase-dephase support, math on external symbols, the ability to define symbols from the console, and many more.

HOW FAST IS IT? We assembled the file ZCPR3.ASM using three different assemblers on three computers. Here are the results.



TECHNICAL SPECIFICATIONS

- ☐ Assembles to COM, HEX, or REL in one pass
- ☐ Intel macro facility
- ☐ M80/RMAC Compatible (except Zilog mnemonics)
- ☐ Accepts symbol definitions from console
- ☐ Nested conditionals and INCLUDE files
- ☐ Relocatable format supports math on externals
- ☐ Full INTEL mnemonics with Z80.LIB extensions built in
- ☐ Absolute, program, data, and common areas
- ☐ Fastest assembler on the market
- ☐ Sorted symbol and cross-reference tables
- ☐ Labels have 16 significant characters
- ☐ Time and date in listing
- ☐ Assemble for execution at another address
- ☐ User configurable
- ☐ Colons not required on labels
- ☐ Reserved words may be redefined as macros and/or labels
- ☐ SLRMAC + - All internal tables can overflow to disk

COMMAND LINE OPTIONS

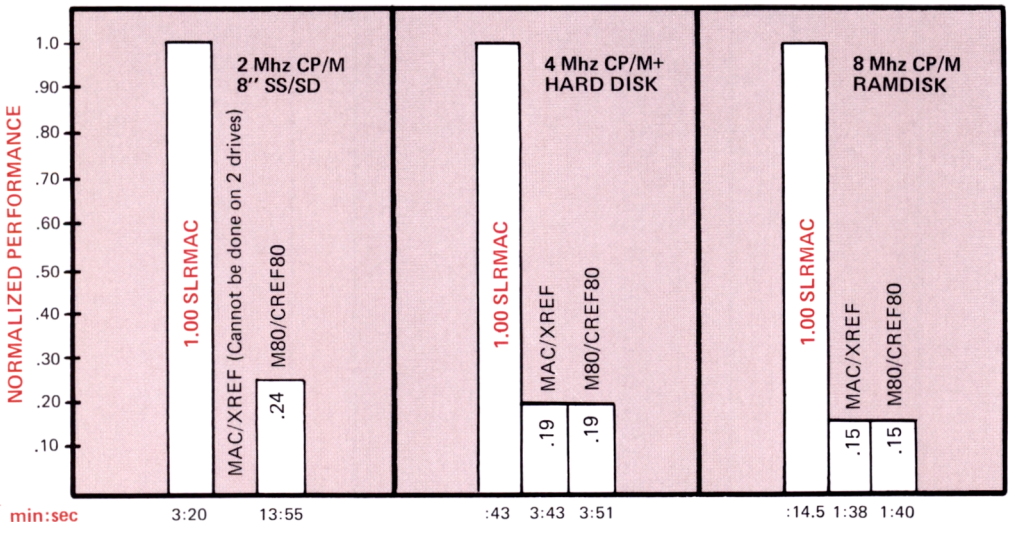
- ☐ Select COM, HEX, SLR Rel, or Microsoft Rel output
- ☐ Select listing output type and output devices
- ☐ Select/Deselect upper/lower case distinction
- ☐ Select 1 or 2 pass mode
- ☐ Select indirect command file
- ☐ Disallow console I/O and auto-detach
- ☐ Select symbol table and/or xref
- ☐ Select auto-declaration of externals
- ☐ Select 6 or 7 characters in Microsoft Rel format

CONFIGURE OPTIONS

- ☐ Set default command line switch settings
- ☐ Set default printer page length and width
- ☐ Control listing of more than 4 bytes of code per source line
- ☐ Set macro listing default
- ☐ Set byte order for printing 16-bit values
- ☐ Flag for suppressing PAGE and TITLE lines in listing
- ☐ Select time/date in listing, with options for clocks not supported directly by the operating system.
- ☐ Use/do not use multi-sector I/O
- ☐ Define listing format
- ☐ Set error page limit and abort limit
- ☐ Number of bytes per HEX record
- ☐ Set all file extensions
- ☐ Set printer initialization and trailer strings

	SLRMAC	MAC/RMAC	M80
Intel Mnemonics	✓	✓	✓
HEX Output	✓	✓	
COM Output	✓		
REL Output	✓	✓	✓
Significant Char. on EXTRN	16	6	6
Math on Externals	✓		SOME
U/L Case Significance	✓		
Time & Date in Listing	✓		
User Configurable	✓		
Sorted Symbol Table	✓	✓	✓
Abort Capability	✓		
Fastest Assembler Available	✓		

WE ASSEMBLED ZCPR3 TO CREATE A LISTING & CROSS REFERENCE.



PSEUDO—OP LIST

ABS	EXITM	LOCAL	.LALL
ASEG	EXT	MACLIB	.LFCOND
ASET	EXTERNAL	MACRO	.LIST
BYTE	EXTRN	MEND	.PHASE
CLIST	GLOBAL	MTLIST	.PRINTX
COM	IF	NAME	.RADIX
COMMON	IF0	NCLIST	.REQUEST
CSEG	IF1	NLIST	.SALL
DATA	IF2	NMTLIST	.SFCOND
DB	IFB	ORG	.TFCOND
DC	IFDEF	PAGE	.XALL
DS	IFDIF	PROG	.XCREF
DSEG	IFE	PUBLIC	.XLIST
DW	IFF	REL	$EJECT
EJECT	IFIDN	REPT	$INCLUDE
ELSE	IFNB	SET	$TITLE
END	IFNDEF	SUBTTL	$+MACRO
ENDIF	IFT	TITLE	$-MACRO
ENDM	INCLUDE	.ACCEPT	$*MACRO
ENT	IRP	.COMMENT	$+PRINT
ENTRY	IRPC	.CREF	$-PRINT
EQU	LIST	.DEPHASE	

SLR Systems

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